

CAREER OBJECTIVE

An enthusiastic Electronics and Communication Engineering graduate with a strong passion as a hardware engineer. Driven by a perpetual pursuit of acquiring new skills, specially in Hardware (PCB designing, signal integrity and power integrity analysis engineer) in software(FPGA programming). Seeking a responsible position in an organization, which gives me a chance to improve my knowledge, enhance skills and enable me to strive towards the overall growth of the organization.

PROFESSIONAL EXPERIENCE

- **Graduate Apprentice at Space Application Centre(SAC), ISRO**
Digital Electronics Division (JAN-2025 to JAN-2026)
 - Contributed to the project, where I was responsible for replacing PROM with MRAM in PCB schematic and layout for digital subsystem development.
 - I have used Cadstar software to implement schematic capture, PCB layout which includes component placement and signal routing with adherence to aerospace standards.
 - I have ensured design integrity by performing DRC (Design Rule Checks) and resolving layout constraints.
 - I have also performed signal integrity analysis and power integrity analysis and improved the layout by resolving problems.
- **Research Internship at Space Application Centre (SAC), ISRO.**
Power Electronics Division (JAN-2024 to APR-2024)
 - I have accomplished my final semester internship at the Space Application Centre (SAC), ISRO. As a research intern at SAC, I contributed to Power Electronics project. I was assigned to the project where I was responsible to understand the requirement and working of the Electronic Power Conditioner (EPC) in spacecraft, including various topologies used in EPC.
 - I mounted components as per requirement, tested circuits, realized waveforms, and verified the turn-on and turn-off delays of the EPC using an oscilloscope. Apart from my project, I also involved in testing of two more pcbs.
- **Summer Internship at INFOLABZ IT SERVICES PVT. LTD.**
Web Development Using Python Django (JULY-2023 to AUG-2023)
 - I have also completed my summer internship at Infolabz IT Services PVT.LTD where I worked as back- end trainee in Python with Django framework. In this internship tenure, I have covered the fundamentals of the Django framework along with an understanding of MVC architecture.

SKILLS

- PCB Schematic and layout
- Signal integrity and power integrity analysis
- S-parameter analysis
- PCB testing
- Web-Development (HTML, CSS, Python Django)
- Excel, MS Word, MS Power Point.
- VHDL basics

SOFTWARES

- Cadstar
- Hyperlynx
- Cadence Allegro
- ADS
- Vivado

EDUCATIONAL BACKGROUND

| BOARD/ UNIVERSITY | COLLEGE/ SCHOOL | STREAM | PASSING YEAR | PERCENTAGE/ CGPA |
|---|---|-------------------------------------|---------------------|-----------------------------|
| Gujarat Technological University, Ahmedabad | G.E.C Gandhinagar, Sector 28 | B.E (Electronics And Communication) | June-2024 | 7.95 |
| GSEB | ST. Xavier's higher secondary school, Gandhinagar | Science | May-2020 | 67.46 |
| GSEB | ST. Xavier's high school, Gandhinagar | General | May-2018 | 77.50 |

LANGUAGES

- English
- Hindi
- Gujarati