

# PRANAV PAREKH

Electronics and communication engineer

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## PROFESSIONAL OVERVIEW

An enthusiastic Electronics and Communication Engineering graduate with a strong passion as a hardware engineer. Driven by a perpetual pursuit of acquiring new skills, specially in PCB designing, signal integrity and power integrity analysis engineer. Seeking a responsible position in an organization, which gives me a chance to improve my knowledge, enhance skills and enable me to strive towards the overall growth of the organization.

## WORK EXPERIENCE

### Graduate Apprentice

Space Application Centre, ISRO | 01/2025 - 01/2026

- Contributed to the project, where I was responsible for replacing PROM with MRAM in PCB schematic and layout for digital subsystem development.
- I used Cadstar software to implement schematic capture, PCB layout which includes component placement and signal routing with adherence to aerospace standards.
- I ensured design integrity by performing DRC (Design Rule Checks) and resolving layout constraints.
- I also performed signal integrity analysis(from FPGA to MRAM) and power integrity analysis and improved the layout by resolving problems.

### Research Intern

Space Application Centre, ISRO | 01/2024 - 04/2024

- I was assigned to the project where I was responsible to understand the requirement and working of the Electronic Power Conditioner (EPC) in spacecraft, including various topologies used in EPC.
- I mounted components as per requirement, tested circuits, realized waveforms, and verified the turn-on and turn-off delays of the EPC using an oscilloscope. Apart from my project, I also involved in testing of two more pcbs.

### Summer Intern

Infolabz IT services pvt.ltd | 07/2023 – 08/2023

- I worked as back- end trainee in Python with Django framework. In this internship tenure, I covered the fundamentals of the Django framework along with an understanding of MVC architecture.

## EDUCATION

### B.E in Electronics and communication

G.E.C Gandhinagar  
2020-2024  
CGPA: 7.95

### Higher secondary(12<sup>th</sup> Science)

ST. Xavier's higher Secondary School  
2019-2020  
Percentage: 66.46

### High school(10<sup>th</sup> General)

ST. Xavier's higher Secondary School  
2017-2018  
Percentage: 77.5

## SKILLS

- PCB schematic and layout
- Signal and power integrity analysis
- S-parameter analysis
- VHDL
- Python
- Web Development(HTML,CSS)

## SOFTWARE

- Cadstar
- Cadence (Allegro)
- Hyperlynx
- Vivado
- ADS
- VS code